

L Number	Hits	Search Text	DB	Time stamp
-	31	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (((power adj suppl\$3) near4 current\$1) near\$3 test\$3)).ab.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 11:31
-	1	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (power adj suppl\$3)).ab.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:07
-	931	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (power adj suppl\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:07
-	30	(verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and ((power adj suppl\$3) same (transient\$1 and current\$1 and test\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:43
-	0	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1))).ab.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:51
-	342	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	259	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<20000124	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	57	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<20000124) and (logic adj signal\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	1	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<20000124) and (logic adj signal\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:58
-	7	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<20000124) and (logic adj signal\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	36	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	24	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1)))) and @ad<20000124	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/10/28 17:59
-	9	5483170.uref.	USPAT	2003/11/02 19:22
-	8	5483170.uref. and @ad<20000124	USPAT	2003/11/02 19:38
-	4	(5483170.uref. and @ad<20000124) and transient	USPAT	2003/11/02 19:57
-	1	5850404.pn.	USPAT	2003/11/02 19:57

-	1	5321354.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 11:52
-	1	5321354.pn. and transient	USPÄT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 17:05
-	234	702/58.ccls.	USPÄT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 17:07
-	151	702/118.ccls.	USPÄT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 17:07
-	130	702/120.ccls.	USPÄT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/05 17:07
-	424	714/719.ccls.	USPAT	2003/11/05 17:08
-	153	714/720.ccls.	USPAT	2003/11/05 17:08
-	161	714/721.ccls.	USPAT	2003/11/05 17:09
-	64	714/728.ccls.	USPAT	2003/11/05 17:09
-	226	714/735.ccls.	USPAT	2003/11/05 17:09
-	596	714/736.ccls.	USPAT	2003/11/05 17:09
-	70	714/737.ccls.	USPAT	2003/11/05 17:09
-	111	714/741.ccls.	USPAT	2003/11/05 17:13
-	2033	702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.	USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT	2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13 2003/11/05 17:13
-	594	(702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)	USPAT	2003/11/05 17:47
-	0	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and (transient\$1 with power with suppl\$3 with current)	USPAT	2003/11/05 17:16
-	9	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and ((transient\$1) same (power and suppl\$3 and current\$1))	USPAT	2003/11/05 17:17
-	12	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and ((transient\$1) same ((power or suppl\$3) and current\$1))	USPAT	2003/11/05 17:44

-	10	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and (((transient\$1) same (power or suppl\$3)) same current\$1)	USPAT	2003/11/05 17:44
-	464	(test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))	USPAT	2003/11/05 17:48
-	34	((test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))) and ((power adj supply) with current\$1)	USPAT	2003/11/05 17:52
-	6	((test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))) and (transient same ((power adj supply) with current\$1))	USPAT	2003/11/05 18:18
-	4	cadence.as. and CAE	USPAT	2003/11/05 18:46
-	4	quickturn.as. and CAE	USPAT	2003/11/05 18:48
-	91	(ISHIDA-MASAHIRO YAMAGUCHI-TAKAHIRO).in.	USPAT	2003/11/05 18:49
-	7	((ISHIDA-MASAHIRO YAMAGUCHI-TAKAHIRO).in.) and fault and simulation	USPAT	2003/11/05 19:26
-	2048	(signal with line) and (path with delay) and (gate with delay)	USPAT	2003/11/05 19:32